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S. Aniruddhan: Home

I Am An Associate Professor In The VLSI Group Of The Department Of Electrical Engineering At Indian Institute Of Technology Madras. My Students Work Broadly In The Area Of Analog IC Design, With Specific Focus On RFIC Design. I Obtained A B. Tech. Degree In Electrical Engineering From IIT Madras In 2000, And A Ph.D. Degree From The University Of Washington, Seattle In 2006. Mar 15th, 2019

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